

HMI APPLICATION NOTE

68000 Target Clock Buffer

Newer versions of the HMI-200-68000 emulator have a DIP switch and a 7408 IC located on the target interface adapter. This allows the user to buffer the target clock signal. Enabling the buffer will prevent the emulator's cable and internal circuitry from loading down the target clock signal, but it will also introduce one gate delay on the clock signal.

To enable the target clock buffer both switches should be set so that the raised part of the rocker is away from the numbers on the switch's housing.

To disable the target clock buffer both switches should be set so that the raised part of the rocker is towards the numbers on the switch's housing.